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Form PTO-1449 (Rev. 8-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO: 0103376-00003	APPLICATION NO: 09/929,877
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>		APPLICANT(S): Afek et al.	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE*

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

25	A	Bennett, J.C.R. et al. "Hierarchical Packet Fair Queueing Algorithms."
	B	Bennett, J.C.R. et al. "High Speed, Scalable, and Accurate Implementation of Fair Queueing Algorithms in ATM Networks."
	C	Bennett, J.C.R. et al. "WF ² Q: Worst-case Fair Weighted Fair Queueing."
	D	Chiussi, F.M. et al. "Implementing Fair Queueing in ATM Switches: The Discrete-Rate Approach."
	E	Chiussi, F.M. et al. "Minimum-Delay Self-Clocked Fair Queueing Algorithm for Packet-Switched Networks."
	F	Demers, A. et al. "Analysis and Simulation of a Fair Queueing Algorithm," © 1989 Association for Computing Machinery.
	G	Eckhardt, D.A. et al. "Effort-limited Fair (ELF) Scheduling for Wireless Networks," IEEE INFOCOM 2000.
	H	Golestani, S.J. "Network Delay Analysis of a Class of Fair Queueing Algorithms," IEEE Journal on Selected Areas in Communications, vol. 13 no. 6 (August 1995) pp. 1057-1070.
	I	Golestani, S.J. "A Self-Clocked Fair Queueing Scheme for Broadband Applications," IEEE © 1994 pp. 5c.1.1-5c.1.11.
	J	Greenberg, Albert G. et al. "How Fair is Fair Queueing?" Journal of the Association for Computing Machinery vol. 39 no. 3 (July 1992) pp. 568-598.
	K	Parekh, A.K.J. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks," Ph.D. Dissertation Massachusetts Institute of Technology (February 1992).
	L	Parekh, A.K. et al. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks: The Multiple Node Case," IEEE/ACM Transactions on Networking vol. 2 no. 2 (April 1994) pp. 137-150.
25	M	Parekh, A.K. et al. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks: The Single-Node Case," IEEE/ACM Transactions on Networking vol. 1, no. 3 (June 1993) pp. 344-357.

FT	N	"Quality of Service Networking," downloaded from the web (address: http://www.cisco.com/univercd/cc/td/doc/cisintwk/ito_doc/qos.htm) © Cisco Systems, Inc.
FT	O	Rexford, J.L. et al. "Hardware-Efficient Fair Queuing Architectures for High-Speed Networks," IEEE © 1996 pp. 5d.2.1-5d.2.9.
FT	P	Shreedhar M. et al. "Efficient Fair Queuing Using Deficit Round-Robin," IEEE/ACM Transactions on Networking vol. 4 no. 3 (June 1996) pp. 375-385.
FT	Q	Stiliadis, D. et al. "Frame-based Fair Queuing: A New Traffic Scheduling Algorithm for Packet-Switched Networks," (July 18, 1995) pp. 1-43.
Examiner		Date Considered: 12/05/05
Grant		*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and considered. Include copy of this form with next communication to applicant.

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